



Date: - 20 Feb, 2019

Data Sheet Issue:- A1

Advance data

Insulated Gate Bi-Polar Transistor Type T0425VC33G

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V _{CES}	Collector – emitter voltage	3300	٧
$V_{DC link}$	Permanent DC voltage for 100 FIT failure rate.	1800	V
V_{GES}	Peak gate – emitter voltage	±20	V

	RATINGS	MAXIMUM LIMITS	UNITS
I _{C(DC)}	DC collector current, IGBT	425	Α
I _{CRM}	Repetitive peak collector current, tp=1ms, IGBT	850	Α
I _{F(DC)}	Continuous DC forward current, Diode	425	Α
I _{FRM}	Repetitive peak forward current, tp=1ms, Diode	850	Α
I _{FSM}	Peak non-repetitive surge t _p =10ms, V _{RM} =60%V _{RRM} , Diode (Note 4)	2545	Α
I _{FSM2}	Peak non-repetitive surge t _p =10ms, V _{RM} ≤10V, Diode (Note 4)	2800	Α
P _{MAX}	Maximum power dissipation, IGBT (Note 2)	2.75	kW
P _D	Maximum power dissipation, Diode (Note 2)	1.74	kW
(di/dt) _{cr}	Critical diode di/dt (note 3)	1000	A/µs
Tj	Operating temperature range.	-40 to +125	°C
T _{stg}	Storage temperature range.	-40 to +125	°C

Notes: -

- 1) Unless otherwise indicated $T_j = 125$ °C.
- 2) $T_{sink} = 25^{\circ}C$, double side cooled.
- 3) Maximum commutation loop inductance 1000nH.
- 4) Half-sinewave, 125°C T_i initial.



Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
\/	Collector emitter acturation voltage	-	2.65	2.95	I _C = 425A, V _{GE} = 15V, T _j = 25°C	V
V _{CE(sat)}	Collector – emitter saturation voltage	-	3.4	3.7	I _C = 425A, V _{GE} = 15V	V
V_{T0}	Threshold voltage	-	-	1.69	Current range: 142 – 425A	V
r_{T}	Slope resistance	-	-	4.74	Current range: 142 – 425A	mΩ
$V_{\text{GE(TH)}}$	Gate threshold voltage	-	5.2	-	$V_{CE} = V_{GE}$, $I_C = 36mA$	V
I _{CES}	Collector – emitter cut-off current		4	11	V _{CE} = V _{CES} , V _{GE} = 0V	mA
I_{GES}	Gate leakage current	-	-	±7	$V_{GE} = \pm 20V$	μA
C _{ies}	Input capacitance	-	58	-	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	nF
$t_{d(on)}$	Turn-on delay time	-	1.6	-		μs
$t_r(V)$	Rise time	-	2	-	I _C =425A, V _{CE} =1800V, di/dt=850A/μs	μs
$Q_{g(on)}$	Turn-on gate charge	-	8.1	-	V _{GE} = ±15V, L _s =500nH	μC
E _{on}	Turn-on energy	-	1.1	-	$R_{G(ON)}$ = 5.1 Ω , $R_{G(OFF)}$ =33 Ω , C_{GE} =183nF	J
$t_{\text{d(off)}}$	Turn-off delay time	-	4.9	-	Integral diode used as freewheel diode	μs
$t_f(I)$	Fall time	-	1.2	-	(Note 3, 4 & 5)	μs
$Q_{g(off)}$	Turn-off gate charge	-	6	-		μC
E _{off}	Turn-off energy	-	1.12	-		J
I _{SC}	Short circuit current	-	1600	-	V_{GE} =+15V, V_{CC} =1800V, V_{CEmax} \leq V_{CES} , t_p \leq 10 μ s	А

Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V	Compared voltage	-	2.65	2.95	I _F = 425A, T _j =25°C	V
V _F	Forward voltage	-	3.0	3.3	I _F = 425A	V
V_{To}	Threshold voltage	-	-	1.72	Current range 142 425A	V
r_{T}	Slope resistance	-	-	3.72	Current range 142 - 425A	mΩ
I _{rm}	Peak reverse recovery current	-	305	-		Α
Q_{rr}	Recovered charge	-	440	-		μC
t _{rr}	Reverse recovery time, 50% chord	-	1.7	-	$I_F = 425A$, $V_{GE} = \pm 15V$, di/dt=850A/ μ s	μs
Er	Reverse recovery energy	-	0.48	-		J

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
		-	-	36.4	Double side cooled	K/kW
R_{thJK}	Thermal resistance junction to sink, IGBT	-	-	59.4	Collector side cooled	K/kW
		-	-	94.3	Emitter side cooled	K/kW
		-	-	57.6	Double side cooled	K/kW
R_{thJK}	Thermal resistance junction to sink, Diode	-	-	88.2	Cathode side cooled	K/kW
		-	-	166	Anode side cooled	K/kW
F	Mounting force	12	-	16	Note 2	kN
W_t	Weight	-	0.65	-		kg

Notes:-

- Unless otherwise indicated T_i=125°C.
 Consult application note 2008AN01 for detailed mounting requirements 1) 2)
- Cost is additional gate emitter capacitance added to output of gate drive Eon integration time 15 μ s from 10% rising Io. Eoff integration time 15 μ s from 90% falling VoE. 3)
- 4) 5)



Curves

Figure 1 – Typical collector-emitter saturation voltage characteristics

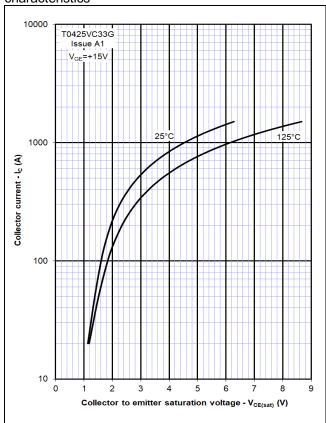


Figure 2 – Typical output characteristic

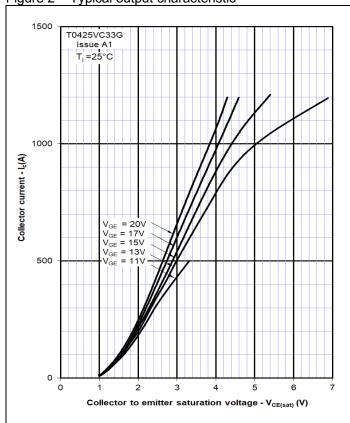


Figure 3 – Typical output characteristic

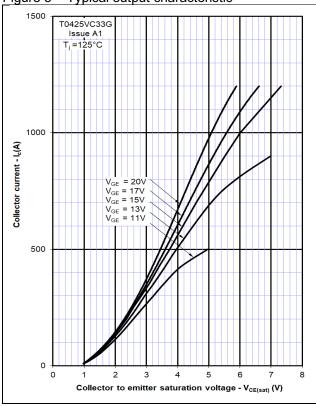


Figure 4 – Typical turn-on delay time vs gate resistance

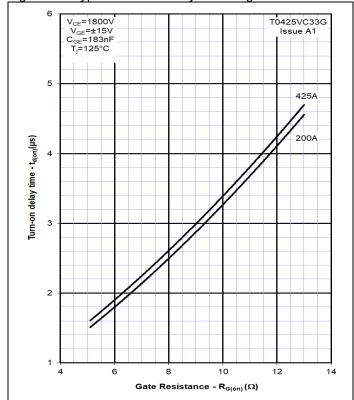




Figure 5 – Typical turn-off delay time vs. gate resistance

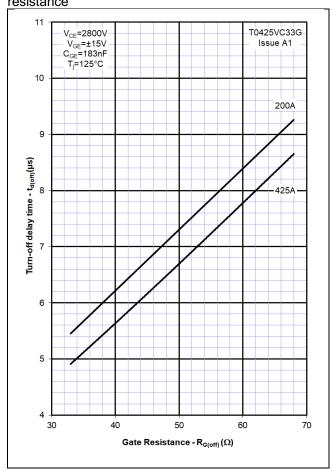


Figure 6 - Typical turn-on energy vs. collector current

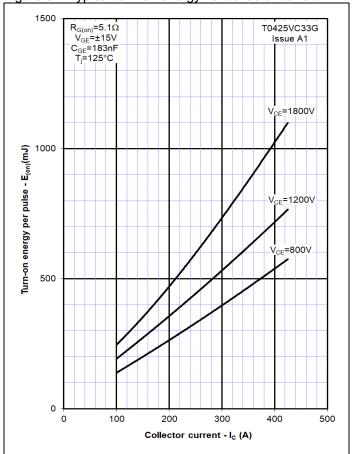


Figure 7 - Typical turn-on energy vs. di/dt

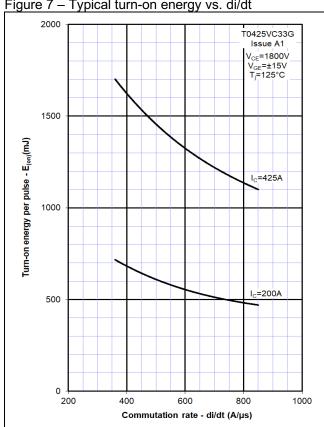


Figure 8 – Typical turn-off energy vs. collector current

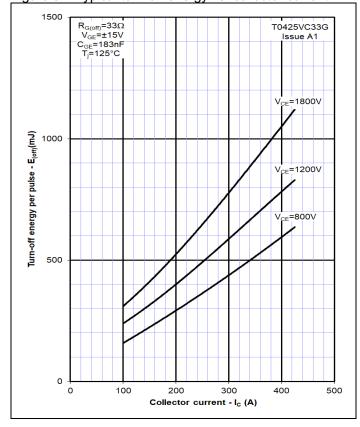




Figure 9 - Turn-off energy vs voltage

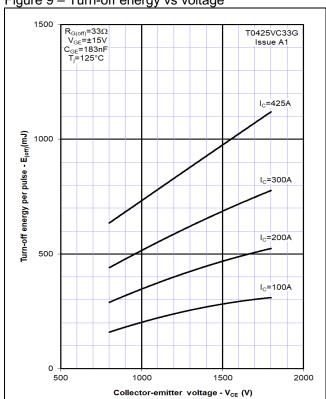


Figure 10 – Safe operating area (IGBT)

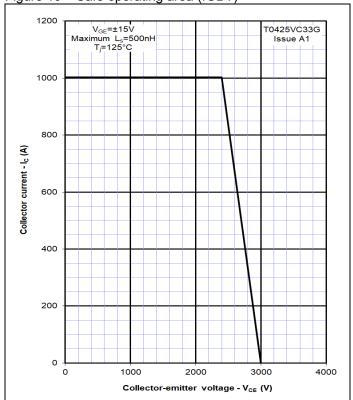


Figure 11 – Typical diode forward characteristics

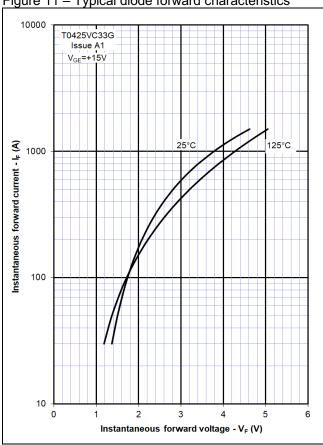
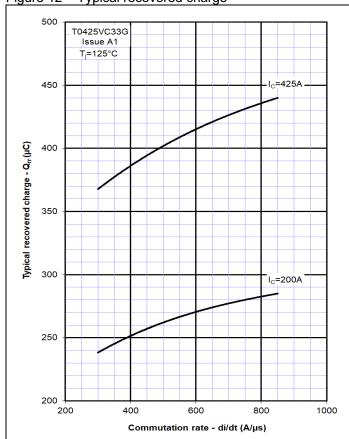
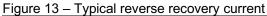


Figure 12 – Typical recovered charge







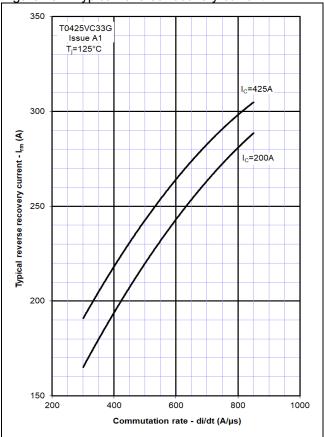


Figure 14 – Typical reverse recovery time

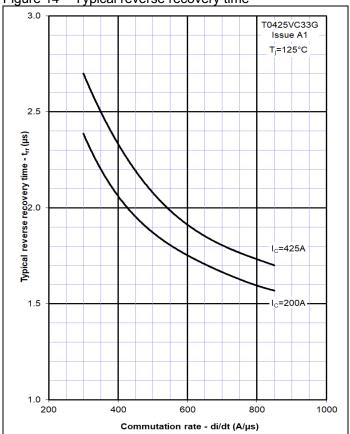


Figure 15 – Typical reverse recovery energy

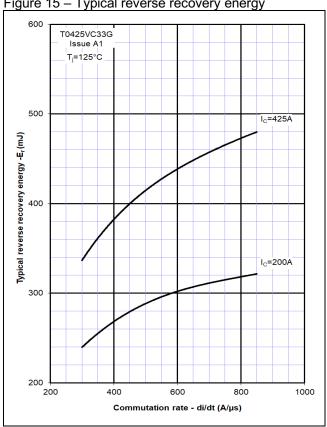


Figure 16 - Safe operating area (Diode)

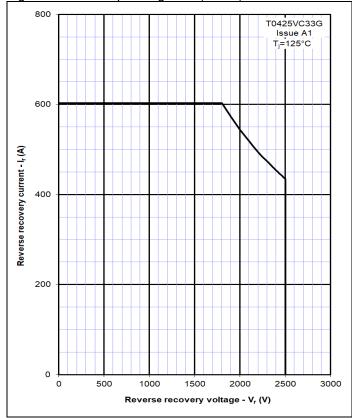




Figure 17 - Transient thermal impedance (IGBT)

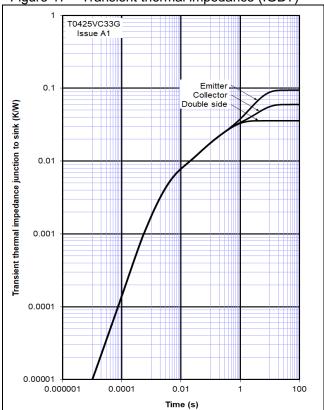
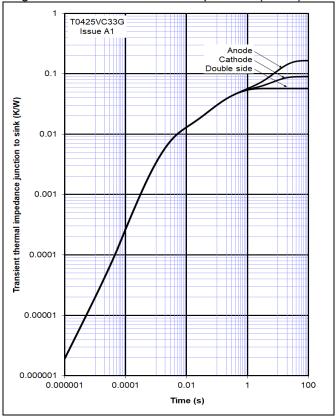
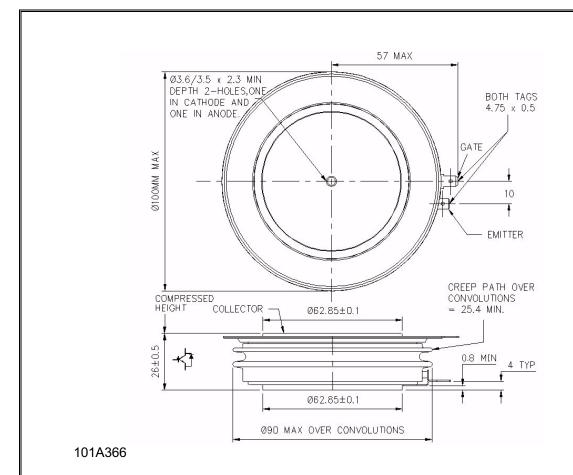


Figure 18 - Transient thermal impedance (Diode)





Outline Drawing & Ordering Information



	ORDERING INFORMATIO	(Please quote	(Please quote 10 digit code as below)			
T0425	VC	33	G			
Fixed type Code	Fixed Outline Code	Voltage Grade V _{CES} /100 33	Fixed format code			
Typical order code: T0425VC33G (V _{CES} = 3300V)						

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