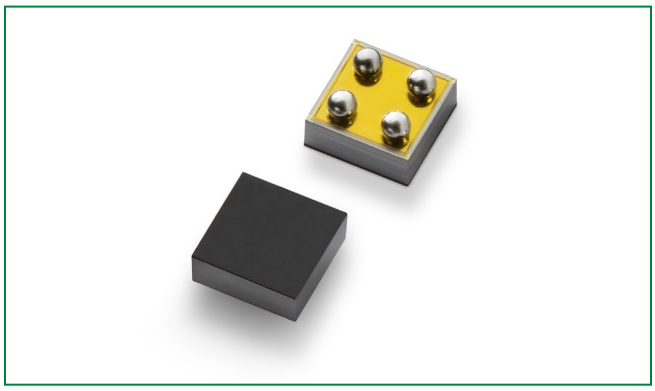
LQ05021RCS4

5 V, 2 A Ultra Low Consumption Load Switch With Reverse Current Blocking

RoHS Pb



Description

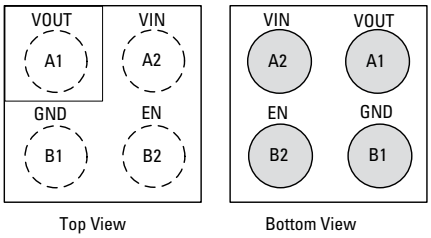
The LQ05021RCS4 represents a cutting-edge, fully integrated high-efficiency load switch device equipped with Reverse Current Blocking (RCB) technology and output voltage slew rate control.

With its leading Reverse Current Blocking (RCB) performance and ultra-low threshold voltage, the LQ05021RCS4 prevents reverse current when V_{OUT} exceeds V_{IN} voltage.

The LQ05021RCS4 offers a typical 37 mΩ RON at 5.5 V, minimizing power loss during operation. Furthermore, it offers a ultra-low shutdown current (I_{SD}) to curtail power wastage and battery drain when in the off state. If EN is pulled low and the output is grounded, the LQ05021RCS4 can achieve a typical I_{SD} as low as 20 nA at 5.5 V.

The LQ05021RCS4 load switch device is designed in a chip scale package of 0.77 mm x 0.77 mm x 0.46 mm with 4 bumps and 0.4 mm pitch and support an extensive input voltage range, enhancing both the operational lifespan and the resilience of the system. Additionally, this single device can serve in various voltage rail applications, streamlining inventory management and lowering operational expenses.

Pinout Designation



Pin Description

Pin #	Pin Name	Description
A1	V_{OUT}	Switch output
A2	V_{IN}	Switch input. Supply voltage for IC
B1	GND	Ground
B2	EN	Enable to control the switch

Features and Benefits

- Wide input range: 1.5 V to 5.5 V, 6 V_{abs} max
- Reverse current blocking
- Ultra-low I_Q: 0.45 µA Typ @ 5.5 V_{IN}
- Ultra-low I_{SD}: 20 nA Typ @ 5.5 V_{IN}
- Low R_{ON}: 37 mΩ Typ @ 5.5 V_{IN}
- I_{OUT} max: 2 A
- Controlled V_{OUT} rise time
- Internal EN pull-down resistor on EN pin
- Integrated output discharge switch

Applications

- Wearables
- Mobile Devices
- IoT Devices
- Low Power Subsystems

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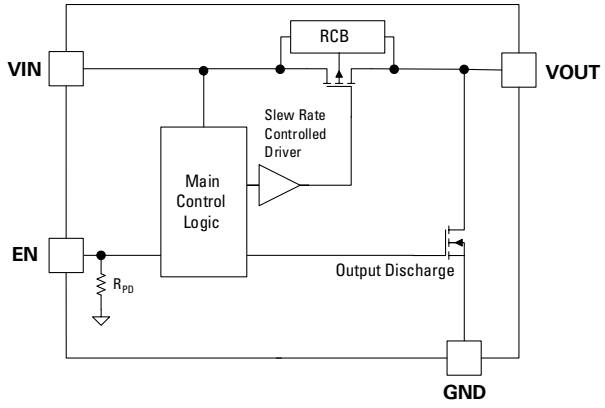
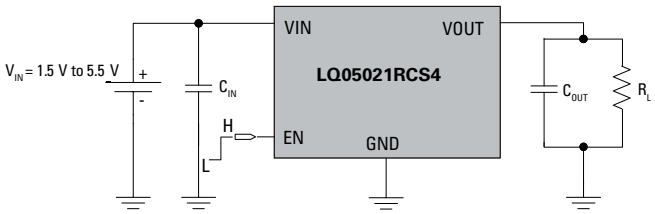
Functional Block Diagram



Typical Applications



Absolute Maximum Rating

Symbol	Parameter		Min	Max	Unit
V_{IN}, V_{OUT}, V_{EN}	Each Pin Voltage Range to GND		-0.3	6	V
I_{OUT}	Maximum Continuous Switch Current			2	A
P_D	Power Dissipation at T_A = 25 °C			1.2	W
T_{STG}	Storage Junction Temperature		-65	150	°C
T_J	Maximum Junction Temperature			150	°C
θ_{JA}	Thermal Resistance, Junction to Ambient (board dependent)			85	°C/W
ESD	Electrostatic Discharge Capability	Human Body Model, JESD22-A114	4		kV
		Charged Device Model, JESD22-C101	2		kV

Note: Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions; extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Recommend Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{IN}	Supply Voltage	1.5	5.5	V
T_A	Ambient Operating Temperature	-40	85	°C

Note: The device is not guaranteed to function outside of the recommended operating conditions.

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Electrical Characteristics (Values are at V_{IN} = 3.3 V and T_A = 25 °C unless otherwise noted.)

Symbol	Parameter	Test Conditions		Min	Typ	Max	Unit
Basic Operation							
I_Q	Quiescent Current	EN = Enable, I_{OUT} = 0 mA, V_{IN} = V_{EN} = 5.5 V			980		nA
		EN = Enable, I_{OUT} = 0 mA, V_{IN} = V_{EN} = 5.5 V[1]			450	1000	nA
		EN = Enable, I_{OUT} = 0 mA, V_{IN} = V_{EN} = 5.5 V, T_A = 85 °C[1]			500		nA
I_{SD}	Shutdown Current	EN = Disable, I_{OUT} = 0 mA, V_{IN} = 1.5 V			5		nA
		EN = Disable, I_{OUT} = 0 mA, V_{IN} = 3.3 V			9		nA
		EN = Disable, I_{OUT} = 0 mA, V_{IN} = 4.2 V			12		nA
		EN = Disable, I_{OUT} = 0 mA, V_{IN} = 5.5 V			20	100	nA
		EN = Disable, I_{OUT} = 0 mA, V_{IN} = 5.5 V, T_A = 85 °C			50		nA
R_{ON}	On-Resistance	V_{IN} = 5.5 V, I_{OUT} = 500 mA	T_A = 25 °C		37	42	mΩ
			T_A = 85 °C[4]		43		mΩ
		VI_{IN} = 3.3 V, I_{OUT} = 500 mA	T_A = 25 °C		47	52	mΩ
			T_A = 85 °C[4]		56		mΩ
		V_{IN} = 1.8 V, I_{OUT} = 300 mA	T_A = 25 °C[4]		80		mΩ
		V_{IN} = 1.5 V, I_{OUT} = 100 mA	T_A = 25 °C		100		mΩ
R_{DSC}	Output Discharge Resistance	EN = Low , I_{FORCE} = 10 mA			85		Ω
V_{IH}	EN Input Logic High Voltage	V_{IN} = 1.5 V - 5.5 V		1.2			V
V_{IL}	EN Input Logic Low Voltage	V_{IN} = 1.5 V - 5.5 V				0.45	V
R_{EN}	EN Internal resistance	Pull-down Resistance			10		MΩ
I_{EN}	EN Current	V_{EN} = V_{IN} or GND			0.5		µA
V_{RCB_TH}	RCB Protection Threshold Voltage	V_{OUT} – V_{IN}			25		mV
V_{RCB_RL}	RCB Protection Release Voltage	V_{IN} – V_{OUT}			30		mV
I_{RCB_TH}	RCB Protection Threshold Current	V_{IN} = 3.3 V, Enabled, V_{OUT} > V_{IN}			0.6		A
$t_{Trigger}$	RCB Trigger Time	V_{IN} = 3.3 V, Enabled, V_{OUT} > V_{IN} + 25 mV			266.5		µs
Switching Characteristics[2]							
t_{dON}	Turn-On Delay	R_L = 150 Ω, C_{OUT} = 0.1 µF			430		µs
t_R	V_{OUT} Rise Time				570		µs
t_{dOFF}	Turn-Off Delay[3,4]	R_L = 150 Ω, C_{OUT} = 0.1 µF			17		µs
t_F	V_{OUT} Fall Time[3,4]				15		µs

Notes:

1. I_Q does not include enable pull down current through the pull-down resistor RPD.
2. t_{ON} = td_{ON} + t_R, t_{OFF} = td_{OFF} + t_F
3. Output discharge path is enabled during off.
4. By design; characterized, not production tested.

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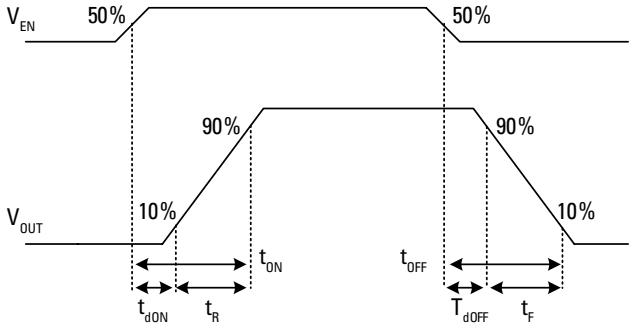
Timing Waveforms



Typical Performance Characteristics

Figure 1 - On-Resistance vs. Supply Voltage

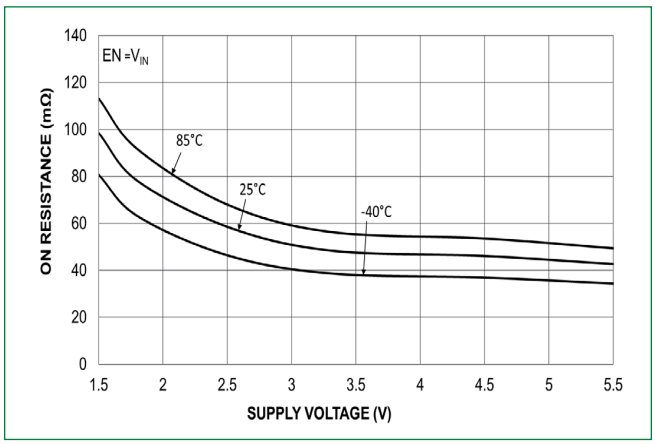


Figure 2 - On-Resistance vs. Temperature

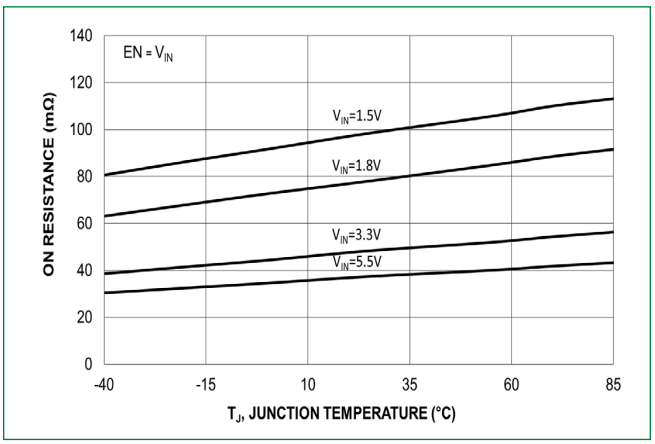


Figure 3 - Quiescent Current vs. Supply Voltage

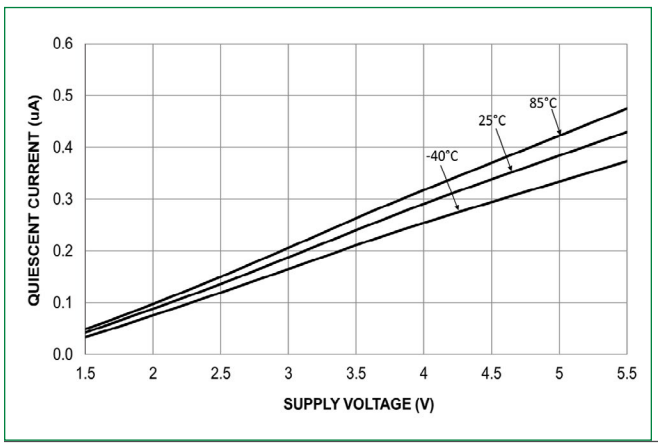


Figure 4 - Quiescent Current vs. Temperature

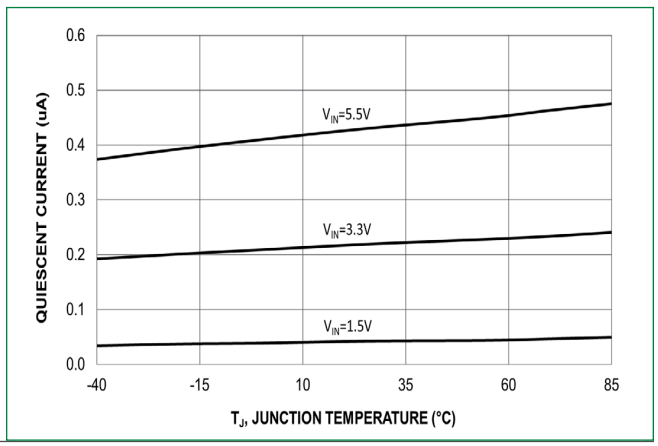


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Figure 5 - Shutdown Current vs. Supply Voltage

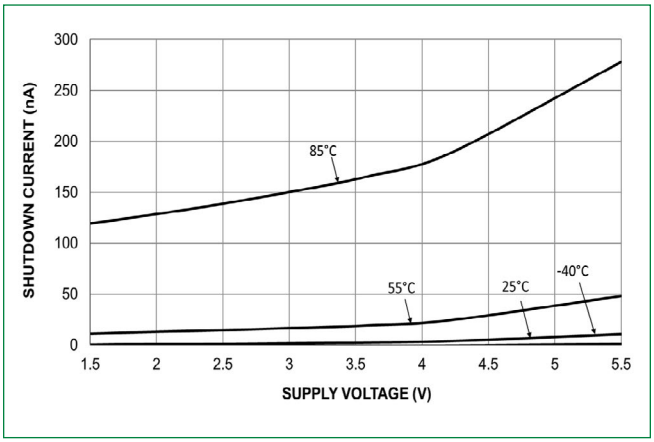

Figure 6 - Shutdown Current vs. Temperature

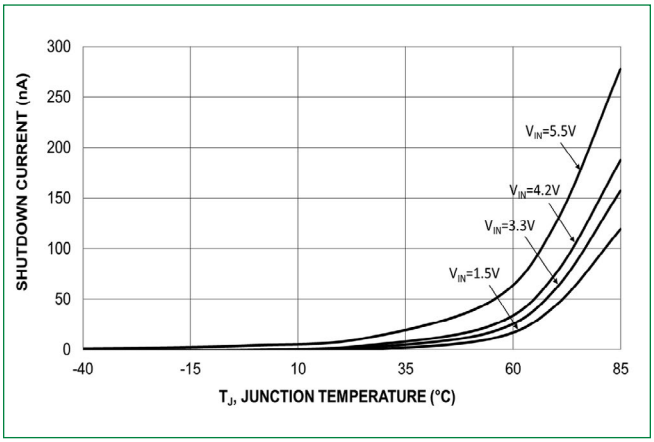

Figure 7 - EN Input Logic High Threshold

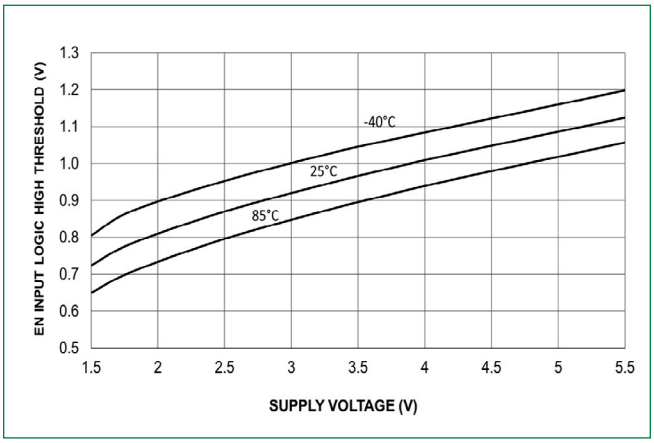

Figure 8 - EN Input Logic High Threshold Vs. Temperature

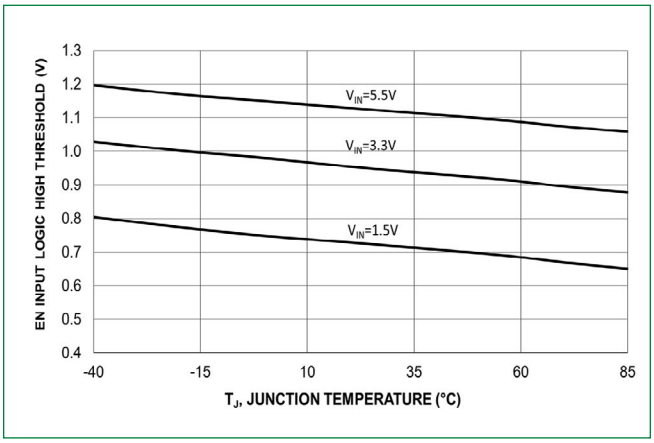

Figure 9 - EN Input Logic Low Threshold

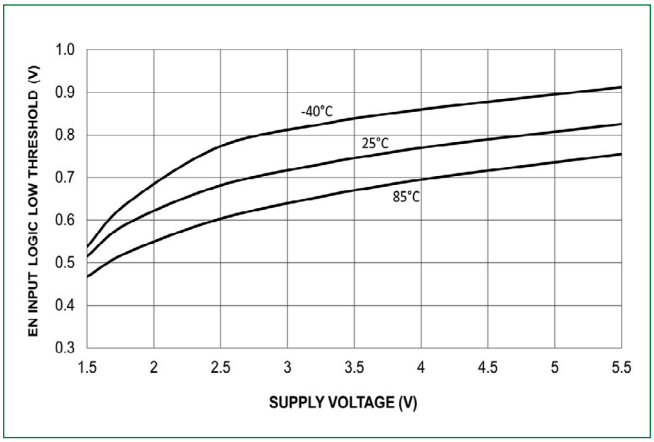

Figure 10 - EN Input Logic Low Threshold Vs. Temperature

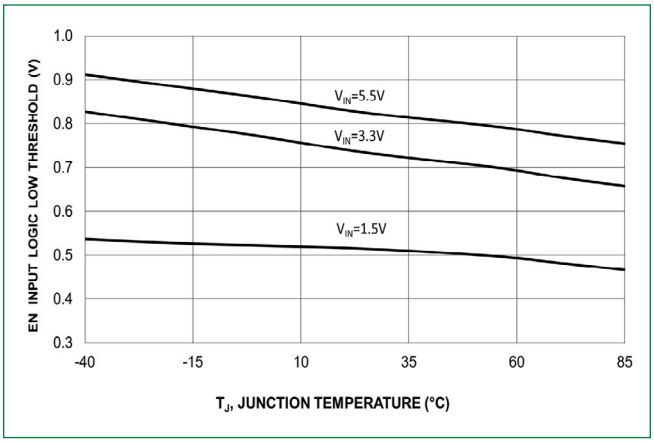

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Figure 11 - V_{OUT} Rise Time vs. Temperature

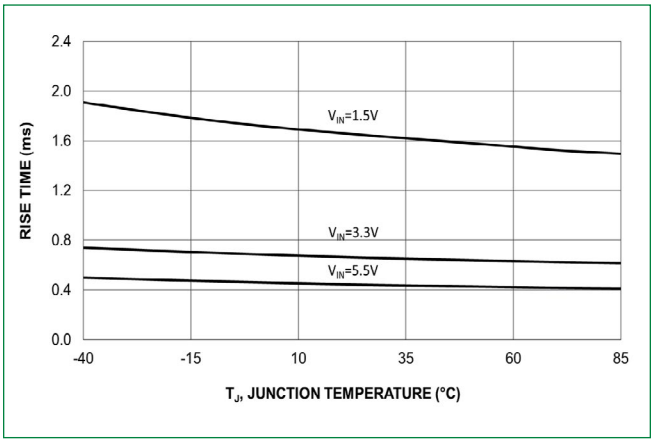


Figure 12 - Turn-On Delay Time vs. Temperature

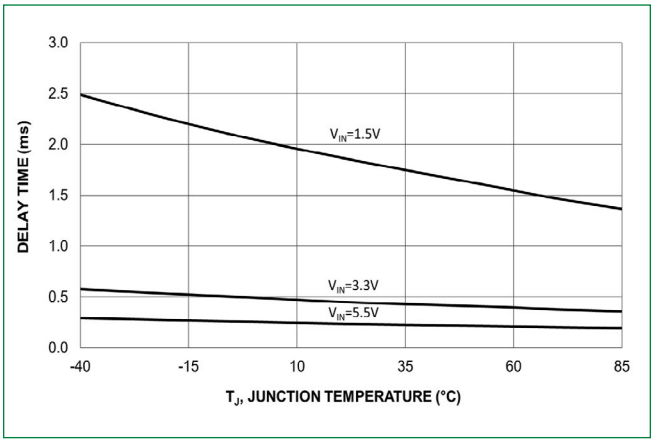


Figure 13 - RCB Threshold Voltage vs. Supply Voltage

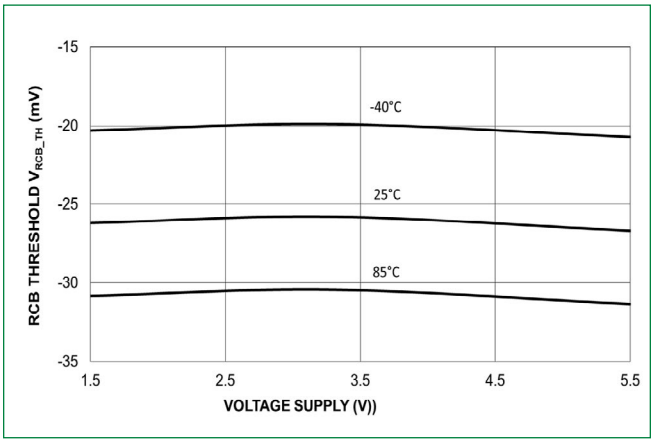


Figure 14 - RCB Threshold Voltage vs. Temperature

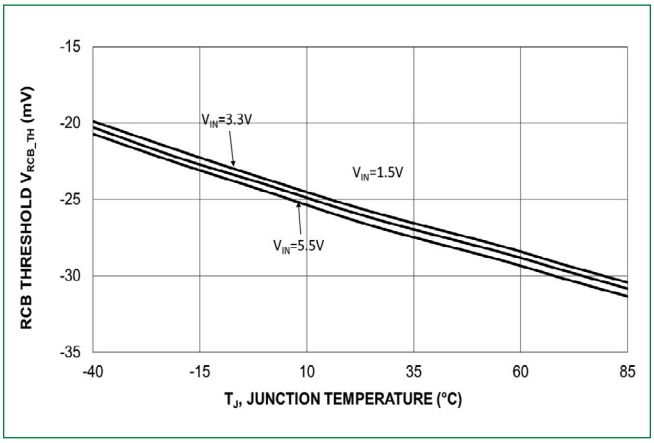


Figure 15 - RCB Release Voltage vs. Supply Voltage

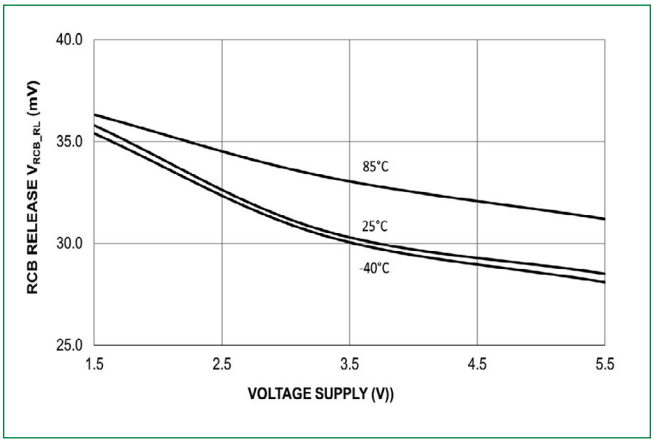


Figure 16 - RCB Release Voltage vs. Temperature

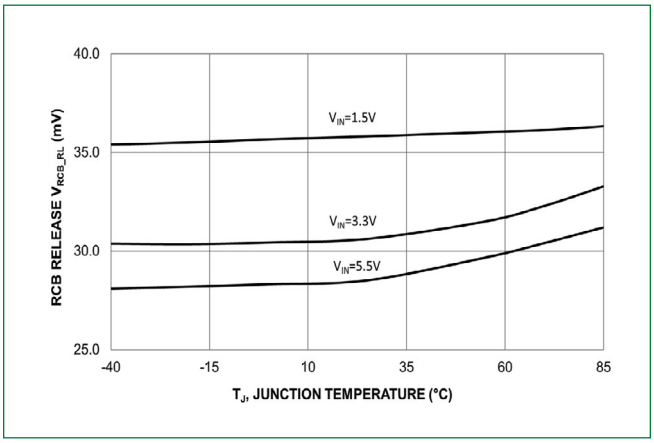


Figure 17 - Turn-On Response
V_{IN} = 3.3 V, C_{IN} = 0.1 µF, C_{OUT} = 0.1 µF, R_L = 150 Ω

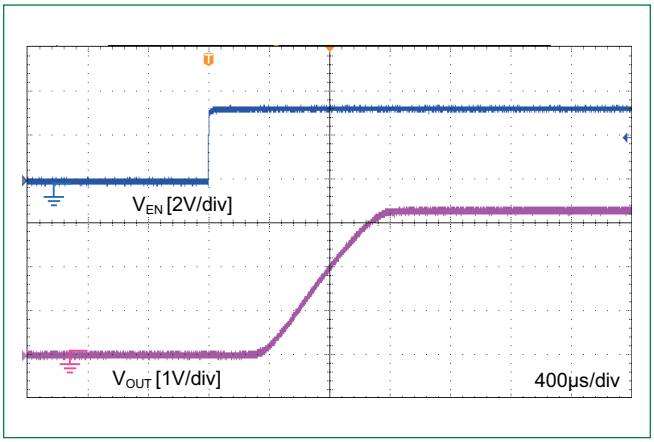


Figure 18 - Turn-Off Response
V_{IN} = 3.3 V, C_{IN} = 0.1 µF, C_{OUT} = 0.1 µF, R_L = 150 Ω

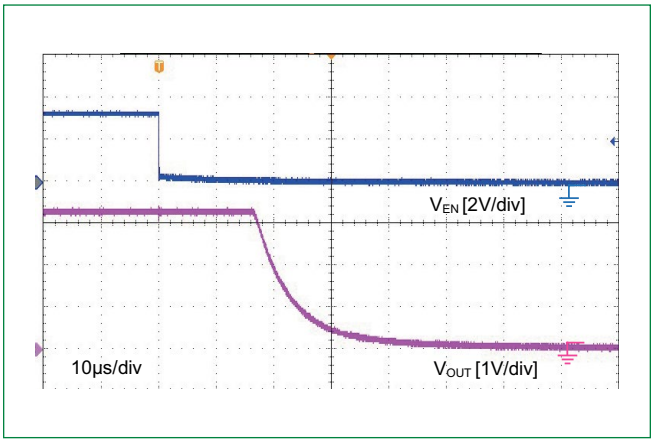


Figure 19 - RCB Release Voltage vs. Supply Voltage

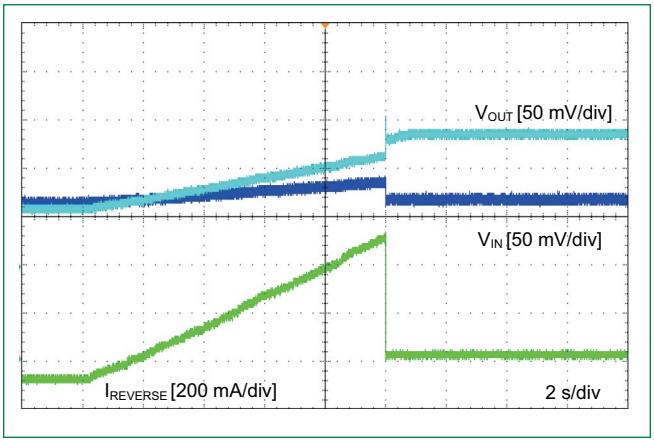
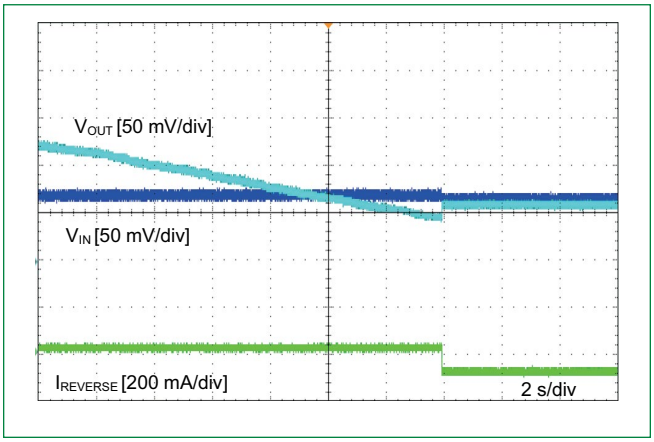


Figure 20 - RCB Release Voltage vs. Temperature



Application Information

The LQ05021RCS4 is a highly efficient integrated load switch with a 2 A capacity. It allows a fixed slew rate control to limit inrush current when activated. This device works with a wide input voltage range, from 1.5 V to 5.5 V, and has minimal on-resistance to reduce power loss. When it is off, it has very low leakage current, saving power resources. It is in a chip scale size package at 0.77 mm x 0.77 mm x 046 mm with 4 bumps at a 0.4 mm pitch make it ideal for efficient manufacturing in the space-saving required applications.

Input Capacitor

Although this is not required to have an input capacitor. Suggest to use a 0.1 µF capacitor positioned near the V_{IN} pin to address voltage fluctuations on the input power rail that may occur as a result of transient inrush current during startup. To reduce the extent of the input voltage drop, suggest to use a higher input capacitor value.

Output Capacitor

A capacitor with a value of 0.1 µF or higher is capable of preventing undershoot caused by parasitic inductance in onboard traces when the circuit is powered off, thus enhancing the reliability of a regulated voltage supply. The C_{OUT} should be positioned in close position to the V_{OUT} and GND pins.

LQ05021RCS4

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EN pin

The LQ05021RCS4 can be turned on by setting the EN pin to a high level. Be aware that there is an internal pull-down resistor in EN pin which can pull the primary switch to “off state” as long as no EN signal from an external controller is applied.

Reverse Current Blocking

The LQ05041RCS6 incorporates a built-in reverse current blocking protection feature that continuously monitors the output voltage level, irrespective of the status of the EN pin. Its purpose is to verify if the output voltage exceeds the input voltage.

When the output voltage surpasses the input voltage by 25 mV, known as the trip voltage for reverse current blocking protection, the function responsible for reverse current blocking deactivates the switch.

It’s important to note that some reverse current may persist until the V_{RCB} is triggered.

The switch will return to normal operation when the output voltage falls below the input source by the RCB protection release voltage.

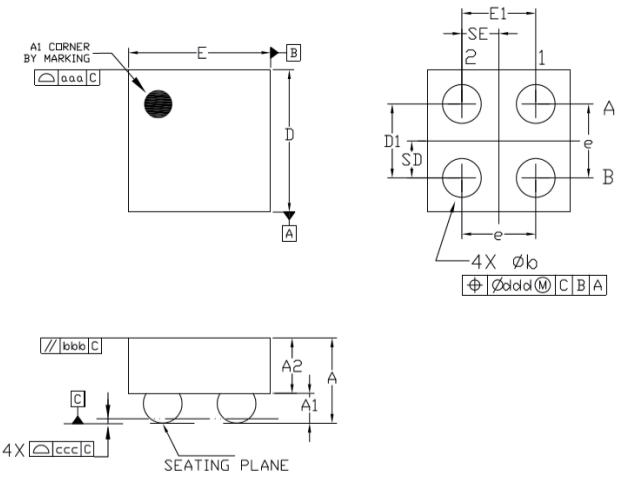
Output Discharge Function

The device incorporates an internal discharge N-channel FET switch located at the VOUT pin. When the EN signal switches the primary power FET to an off state, the N-channel switch activates to rapidly discharge the output capacitor.

Board Layout

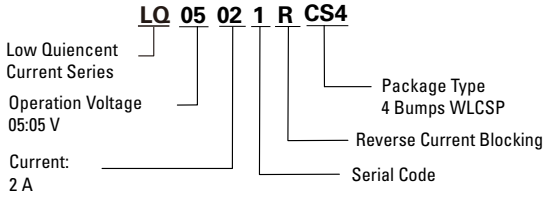
To minimize the impact of parasitic inductance, it is advisable to keep all traces as short as possible. Using wider traces for V_{IN}, V_{OUT}, and GND is recommended to mitigate parasitic effects during dynamic operations and enhance thermal efficiency under high load currents.

Dimensions

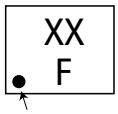


Dimension	Millimeters		
	Min	Nom	Max
A	0.410	0.460	0.510
A1	0.135	0.160	0.185
A2	0.275	0.300	0.325
D	0.755	0.770	0.785
E	0.755	0.770	0.785
D1	0.350	0.400	0.450
E1	0.350	0.400	0.450
b	0.170	0.210	0.250
e	0.400 BSC		
SD	0.200 BSC		
SE	0.200 BSC		
Tol. of Form & Position			
aaa	0.100		
bbb	0.100		
ccc	0.050		
ddd	0.050		

Part Numbering



Part Marking



Pin 1 mark

F = Device Code
XX = Lot Run Code

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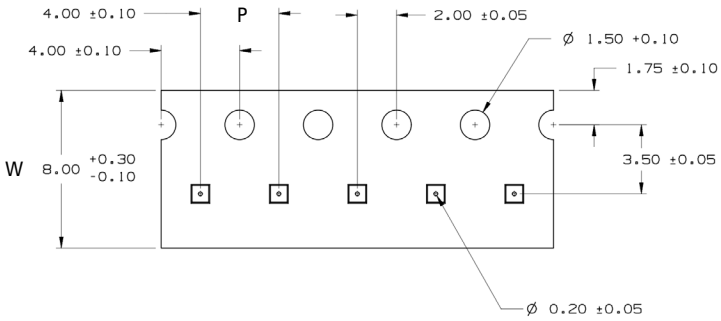
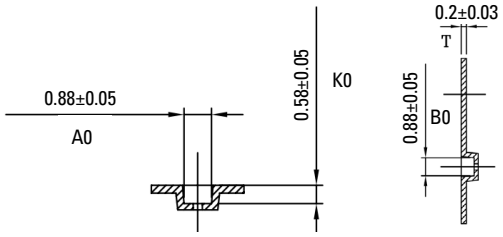
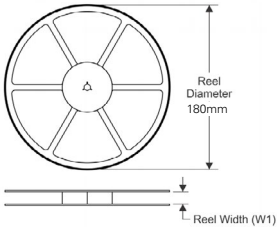
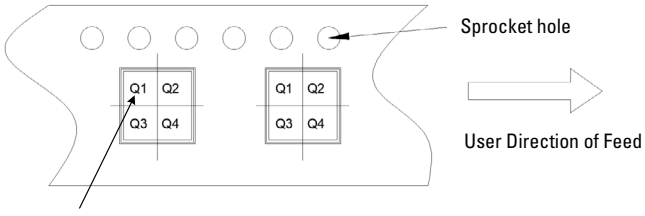
Carrier Tape & Reel Specification









Quandrant assignments pin 1 orientation
Dimensions are in millimeters

Device	Package	Pins	SPQ	Reel Diameter	Reel Width W1	A0	B0	K0	P	W	Pin1
LQ05021RCS4	4 Bumps WLCSP	4	4000	179	9	0.88	0.88	0.58	4	8	Q1