

SP4023 Series 1.3pF, 12A Discrete TVS Diode









Description

The SP4023 Series integrate low capacitance steering diodes with one or two avalanche breakdown diodes for unidirectional or bidirectional protection, respectively, to protect against ESD and lightning induced surge events. These components can safely absorb up to 12A per IEC 61000-4-5 2^{ND} Edition (t_=8/20µs) without performance degradation and a minimum ±30kV ESD per IEC 61000-4-2 International Standard. The low loading capacitance and high surge capability make it ideal for protecting telecommunication ports such as xDSL and other high voltage, high speed legacy interfaces.

Pinout



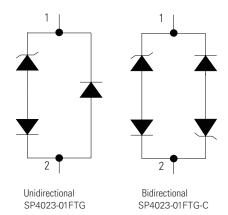
Cathode polarity for unidirectional only

Features

- ESD, IEC 61000-4-2, ±30kV contact, ±30kV air
- EFT, IEC 61000-4-4, 40A (5/50ns)
- Lightning, 12A (8/20µs as defined in IEC 61000-4-5 2nd edition)
- Low capacitance of 1.3pF $(@V_{R}=0V)$
- Low leakage current

- Unidirectional and Bidirectional configuration
- Small SOD323 package fits 0805 footprints
- AEC-Q101 qualified
- · Halogen free, Lead free, and RoHS Compliant
- Moisture Sensitivity Level (MSL-1)

Functional Block Diagram



Applications

- xDSL Interfaces
- RS-232
- RS-485
- Power Ports
- Security Equipment
- Instrumentation
- Medical Equipment
- Computers and Peripherals

Additional Information



Datasheet



Resources



Samples

Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

Absolute Maximum Ratings

Symbol	Parameter	Value	Units	
l _{pp}	Peak Current (t _p =8/20µs)	12	А	
P _{Pk}	Peak Pulse Power (t _o =8/20µs)	450	W	
T _{OP}	Operating Temperature	-40 to 150	°C	
T _{STOR}	Storage Temperature	-55 to 150	°C	

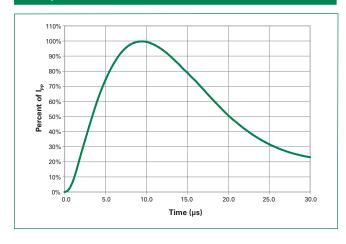
CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the component. This is a stress only rating and operation of the component at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Electrical Characteristics (T_{OP}=25°C)

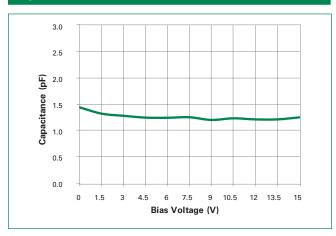
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Reverse Standoff Voltage	V _{RWM}	I _R =1μΑ			15	V
Breakdown Voltage	V _{BR}	I _R =1mA	16			V
Reverse Leakage Current	I _{LEAK}	V _R =15V			0.1	μΑ
Clamp Voltage ¹	V _c	I _{pp} =1A, t _p =8/20μs		23		V
		I _{pp} =2A, t _p =8/20μs		24		V
		I _{pp} =10A, t _p =8/20μs		35		V
		I _{pp} =12A, t _p =8/20μs		37.5	40	
Dynamic Resistance ²	R _{DYN}	TLP, t _p =100ns, I/O to GND		0.55		Ω
ESD Withstand Voltage ¹	V _{ESD}	IEC 61000-4-2 (Contact Discharge)	±30			kV
		IEC 61000-4-2 (Air Discharge)	±30			kV
Diode Capacitance ¹	C _D	Reverse Bias=0V, f=1MHz		1.3	2	pF

1. Parameter is guaranteed by design and/or component characterization.
2. Transmission Line Pulse (TLP) test setting: Std.TDR(50Ω),tp=100ns, tr=0.2ns ITLP and VTLP averaging window: start t1=70ns to end t2=80ns

8/20 µs Pulse Waveform

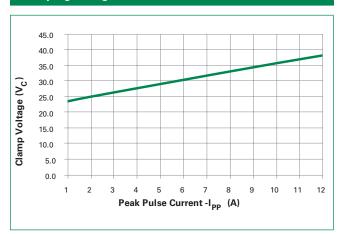


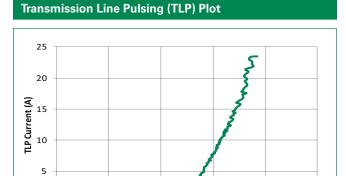
Capacitance vs. Reverse Bias





Clamping Voltage vs. Peak Pulse Current





20

30

TLP Volts (V)

40

50

Soldering Parameters

Reflow Condition		Pb – Free assembly	
	-Temperature Min (T _{s(min)})	150°C	
Pre Heat	-Temperature Max (T _{s(max)})	200°C	
	-Time (min to max) (t _s)	60 - 120 secs	
Average ran	Average ramp up rate (Liquidus) Temp (T _L) to peak		
T _{S(max)} to T _L - Ramp-up Rate		3°C/second max	
Reflow	-Temperature (T _L) (Liquidus)	217°C	
	- Temperature (t _L)	60 – 150 seconds	
Peak Temperature (T _p)		260 ^{+0/-5} °C	
Time within 5°C of actual peak Temperature (t _p)		30 seconds	
Ramp-down Rate		6°C/second max	
Time 25°C t	8 minutes Max.		
Do not exce	260°C		

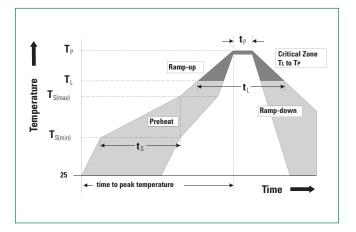
Product Characteristics

10

0

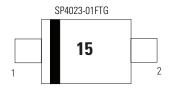
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Lead Plating	Matte Tin
Lead Material	Alloy 42
Lead Coplanarity	0.0004 inches (0.102mm)
Substrate Material	Silicon
Body Material	Molded Compound
Flammability	UL Recognized compound meeting flammability rating V-0



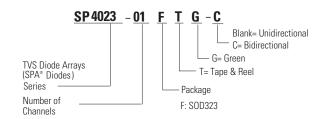


Part Marking System





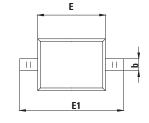
Part Numbering System

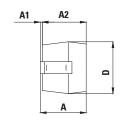


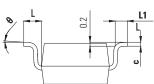
Ordering Information

Part Number	Package	Min. Order Qty.
SP4023-01FTG	SOD323	3000
SP4023-01FTG-C	SOD323	3000

Package Dimensions -SOD323







Recommended Solder Pad

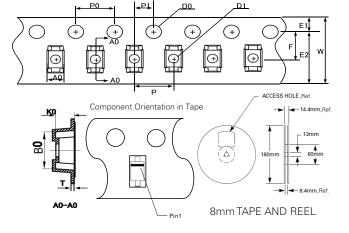
1.79
+ 1.14
+ + 2.28
2.77

Note: Cathode mark is for unidirectional version

Unit: mm

	SOD323				
Symbol	Milli	meters	Incl	hes	
	Min	Max	Min	Max	
Α	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A2	0.80	0.9	0.031	0.035	
b	0.25	0.35	0.010	0.014	
С	0.08	0.15	0.003	0.006	
D	1.20	1.40	0.047	0.055	
E	1.60	1.80	0.063	0.075	
E1	2.50	2.70	0.098	0.106	
L1	0.25	0.40	0.010	0.016	

Embossed Carrier Tape & Reel Specification — SOD323



Symbol	Millimeters
A0	1.46+/-0.10
В0	2.90+/-0.10
W	8.0+0.3/-0.10
D0	1.50+0.10
D1	0.45min/1.15max
E1	1.75+/-0.10
E2	-
F	3.50+/-0.10
P0	4.00+/-0.10
P	4.00+/-0.10
P1	2.00+/-0.05
КО	1.25+/-0.10
Т	0.254+/-0.02